# CpE 213 Example ISM78 – Assembly language programming with µVision2

## Purpose

This is a brief overview of how to use the Keil  $\mu$ Vision2 software to write and debug simple assembly language programs. Only short absolute assembly programs are discussed. The full capability of the A51 macro assembler is not used.

### Description

This example will make use of a small 8051 assembly language program that uses table lookup to translate between a three bit code input through the 8051's P1 and a five bit code to be output on the remaining 5 bits of P1. Specifically, the program specification is to read a three bit code on P1(7 downto 5) and output the square of the code on P1(4 downto 0).

We can read the code with a MOV A,P1 instruction and then shift A right five times to put the code in the least significant bits of A. The instruction ANL A,#7 will set the unused 5 bits to zero. Then we can use the MOVC A,@A+DPTR instruction to do the table lookup. Output the code from the table with the instruction MOV P1,A, then jump back to the beginning to start over. We can use the assembler's DB psuedo-op to construct the table.

The process for using µvision2 to create an application for the 8051 is outlined on page 47 of the Getting Started Guide (gs51.pdf). This process includes:

- a) create a project file and select a cpu,
- b) create an assembler source file and add it to the project,
- c) set tool options for the target hardware,
- d) build the project and create a hex file,
- e) simulate the application with the debugger.

You can find gs51.pdf in the keil/c51/hlp directory or by clicking on the books tab in the project window of µvision2. The project window is the middle left window and the books tab is the rightmost bottom tab. Chapter 3 p39, chapter 4 p47-53, and chapter 5 of gs51 should be read before you try doing much with µvision2. This little handout will help you get started but won't tell you all there is to know about µvision2!

#### Create a project

Start µvision2 from the Start menu. When µvision starts, close any active project with the Project/Close menu item and create a new project with the Project/New menu item. A dialog box like that shown in Figure 1 will be displayed. Use the directory list box and navigation icons to switch to a directory where you have write access. The list in figure 1 shows a subdirectory (cpe213) with several projects (\*.uv2) listed. You are advised to organize your subdirectories systematically and not put all files into a single large directory although the getting started guide's recomendation of a folder per project seems a bit extreme. Use a meaningful name for the project since the default name of the executable is the name of the project file.

After you save the project file, you'll be presented with a dialog box to select the target processor like that shown in Figure 2. The example shows a generic 8051 selected. When you select a target device, a short synopsis of the device's features is displayed. It is instructive to browse through a few of the processors in the  $\mu$ vision database. See if you can find one with two DPTR's.

Create an assembler source file and add it to the project

new file icon



Next, click on the new file icon . This will bring up a text editor window where you can enter your assembler program source code.

Create New F	Project				? ×
Savejn:	🔁 cpe213	•	£	Ċ	<b></b>
ism14.Uv2	🔟 p3-39.Uv2				
ism24.Uv2	🔟 xyz.Uv2				
ism26.0v2					
■ p23.uv2 ■ p3-29.uv2					
p3-31.Uv2					
I					
File <u>n</u> ame:					<u>S</u> ave
Save as type:	Project Files (*.uv2)		•		Cancel
				_	



Select Device for Target 'Target 1'		? ×
CPU Vendor: Generic Device: 8051 (all Variants)	□ Use L251 instead of BL51	
Data base	Description:	
Analog Devices   AnchorChips   Atmel   Dallas   Seneric   8031 (all Variants)   8032 (all Variants)   8052 (all Variants)   C166 (all Variants)   C167 (all Variants)   Hyundai   Infineon   Intel	8051 based CMOS or NMOS Microcontroller with 32 I/O lines, 2 Timers/Counters, 5 Interrupts/2 priorty levels 4 KB ROM, 128 Bytes on-chip RAM	4
	OK Cancel	

Figure 2 Select device dialog box

After you enter your source code, save it using the File/Save As menu item. Save it with a meaningful filename and an 'a51' extension (eg p39.a51) in the same directory as your project file. After you save the file, you can add it to your project. Expand target 1 in the project window by clicking on the '-' box to get a source group 1 entry. Right click on source group 1 and select the 'add files' popup menu item. Select your new a51 source file, click on 'add', then close. Your source file should look something like that in Figure 3.

The first line in this file is a comment started with a semicolon. Line 2 is a cseg psuedo-op that defines a code segment starting at location 0. This is something like the debugger's 'asm 0' command. The first instruction is a long jump (ljmp) to location 'start'. Since all 8051's start up at location 0 after power on, this will take the processor to the first instruction of our program. The next line is a table created with the DB psuedo-op. The table starts at symbolic location 'table' and consists of a string of 8 bytes which are the squares of the first 8 integers (0 through 7). These three lines will result in 11 bytes in locations 0 through 10 of the 8051's code memory. The first three bytes will be 20h, 01h, and 00h which are the machine code for the ljmp instruction. Note that the table MUST be located in code memory. Data memory is sram whose contents are lost after power is turned off. Code memory is non-volatile and is the only place where we can keep initialized data. If we insist on putting initialized data in sram, then we will need to add code to initialize it from code memory. We will never change 'table' so it's best to just keep it in code memory.

```
S:\CPE213\p3-39.a51*
                                                        - 🗆 ×
  example for a p39-like problem
        cseg at 0
        ljmp start
        db 0,1,4,9,16,25,36,49
table:
        cseg at 100h
start:
        mov Pl,#0e0h
                         ; make bits 5 to 7 inputs
loop:
        mov a,pl
                         ; read pl
        mov r2,#5
                         ; shift right 5x (Acc>>5)
rotlp:
        rr a
        djnz r2, rotlp
        anl a,#7
                         ; mask input bits
        mov dptr,#table ; get table address
        movc a, @a+dptr ; get ith entry in table
        orl a,#OeOh
                         ; make high order bits = 1
        mov pl,a
                         ; output to pl
        sjmp loop
                         ; repeat forever
        end
€ |
```

Figure 3 Example assembler source file

The second cseg is there to move the location counter above the interupt vectors which occupy lower code space. We don't really need it this time but it's a good habit to get into. The symbol 'start' defines the location of the mov instruction which is the target of the initial ljmp. We could have simply said ljmp 100h but that isn't good practice. It's better to use symbols than bare constants.

The mov P1,#0e0h instruction makes the upper three bits of port 1 inputs. Recall that writing a '0' to an 8051 port bit turns on its pulldown and writing a '1' turns the pulldown off. We want the external device (a switch pulldown and resistor pullup combination) to determine whether the port bit is a '1' or a '0' so this instruction is there just to make sure the port bit is acting as an input and not an output. The other 5 bits are set to 0's. This is arbitrary in this example. In a particular application we may want to set them to 1's as well.

The next instruction (labeled 'loop') is the start of a large loop that reads the 3 bit input, shifts it to the least significant bits of the accumulator, does a table lookup to calculate the square of the 3 bit number, and outputs the resulting 5 bit number on the other 5 bits of port 1. The next three instructions shift the accumulator right 5 bits. Notice that this takes 16 cycles to execute and requires 5 bytes of code. Five rr instructions would also require five bytes of code and run in only 5 cycles. This program has room for improvement!

Once the 3 bit input number is in the least significant bits, we clear the other 5 bits to 0's with the anl, put the address of the lookup table into dptr, and then use an indexed movc instruction to load the square of the 3 bit number into the accumulator. Finally we set the 3 most significant bits to 1's so we don't turn our input port into an output port, output the square to P1 (keeping the upper three bits as inputs), and repeat the whole process.

Select tool options

Before building the project, right click on the target 1 box and select 'select tool options' from the popup menu. You'll get a dialog box like that in Figure 4. Select the 'debug' tab and make sure the defaults are set like that in figure 4. We will be using the simulator and we want the application loaded when the simulator starts. Under the 'output' tab you can select 'create hex file'. It's not required but will give you a hex file to look at. Microvision's simulator uses the linker output. The hardware simulator we use in CpE 214 uses the hex file. PROM programmers also use the hex file format.

Build the project application

Click on the build target icon to assemble the source file and create an object file. If there are any errors, fix them and rebuild the target. If there aren't any errors (there shouldn't be), it's instructive to create one just to see what happens. Try taking the last 't' off one of the 'start' symbols and rebuild.

Debug the application

debug icon

Build icon

Click on the debug icon to start up the debugger with your application. The project window will switch to the register tab and display the 8051's registers and the text window with your code will display a yellow arrow that points to the next instruction to be executed (the ljmp). Open up the P1 window by selecting the Peripherals/IO Ports/Port 1 menu item. Press F11 twice and notice what happens: the location arrow moves to 'loop' and the two instructions that are executed get marked with a green mark. These 'code coverage' marks show how much of your program has been executed and are an aid in testing complex programs. When the mov instruction is executed, the Port 1 window reveals that P1 bits 5, 6, and 7 are set and the remaining bits are reset. Notice that there is a row of 'pins' bits and a row for P1. The P1 bits correspond to the port's flip flops that are set/reset by the mov instruction, while the 'pins' bits reflect the actual state of the pins. Try clicking on pin 0 and see what happens. If you set a real processor's P1.0 to 0 and then tried to force the P1.0 pin high you wouldn't get a warning message. Most likely what would happen is that the port bit driver would quietly die and you'd be left with a damaged microcontroller. You can click on P1.0 (the top row) to set it and this will have exactly the same effect as a setb P1.0 instruction. You can do this to any of the registers and memory locations if you need to during debugging. Now press the reset icon to reposition the location arrow back at location 0.

reset icon

RST

Options for Target 'Target 1'	? ×				
Target Output Listing C51 A51 L51 Locate L51 Misc Debug					
C Use Simulator	C Use: Keil Monitor-51 Driver Settings				
☑ Load Application at Startup ☑ Go till main()	Load Application at Startup 🔲 Grot till main()				
Initialization File:	Initialization File:				
Browse	Browse				
Restore Debug Session Settings Breakpoints IV Toolbox Watchpoints & PA Memory Display	Restore Debug Session Settings Breakpoints Toolbox Watchpoints Memory Display				
CPU DLL: Parameter:	Driver DLL: Parameter:				
Dialog DLL: Parameter:	Dialog DLL: Parameter:				
DP51.DLL -p51	TP51.DLL -p51				
OK Cancel Defaults					

Figure 4 Target options dialog box.

Now select the View/Disassembly window to bring up a window that shows the assembly code along with the machine code. Notice that location c:0 contains a 02h, c:1 contains 01h, c:2 contains 00h and so on. Notice also that the 8 bytes in 'table' have also been dutifully disassembled as if they were instructions. The disassembler doesn't know any better. Adjust the register window so that the accumulator is visible. Press F11 until the mov instruction at location 103 is executed. Notice that the A register changes to an E0h and is colored green to highlight the fact that it has changed. Registers that change as a result of instruction execution get highlighted this way.

We could just keep hitting F11 and execute one instruction at a time but that is a hard way to debug a million lines of code. Breakpoints are better. Double click on the sjmp instruction in the disassembly window and notice that a red dot appears in the left margin. This is a breakpoint. If we now run the program, it will stop when it hits the breakpoint. Go ahead and hit F5 (run) and notice that the yellow location arrow stops at the breakpoint. Notice also that a is now equal to F1. Is this correct? Unless you changed P1 you should have read an input of 7 (upper 3 bits are one by default), read a 49 from the table (31h), and tried to output it. Unfortunately 31h is one bit too large for 5 bits. We need 6 bits. We now have a dilemma. We need to read 3 bits and output 6 but our ports are only 8 bits. There are many ways to solve this and which way is 'best' depends a lot on other factors that haven't been specified. For example, let's assume that we've already fabricated printed circuit boards and reassigning the 3 input bits to a different port is out of the question. If a spare port bit isn't available we are really in hot water and probably should start looking for another job. Let's assume that P3.0 is free and use it to output the missing 6<sup>th</sup> bit in our output code. We can do that by moving ACC.5 to P3.0 before the ORL is executed.

Click the debug icon to go back to the editor mode and add the two instructions: mov c,acc.5 and mov P3.0,c to your source code just before the ORL instruction. Build the application as before and click debug to get back into debug mode. Now open up both P1 and P3 port windows.

Instead of stepping through using F11 or F5 and breakpoints, this time we'll just run the program. Select the View/Periodic Window Update item from the menu. This will periodically update the port windows as the program runs. With both P1 and P3 visible, hit the F5 key to run continuously. Now try entering different bit patterns into P1 bits 5, 6, and 7. Be sure to click on the pins check boxes and not the port boxes. Clearing the check box forces the pin low and will result in the mov a,p1 instruction reading a 0 in that bit position. Checking the box causes a 'l' to be read. As you enter each number, make sure that the 5 low order bits of P1 and P3.0 display the square of the 3 bit number in the P1 input field. If you got that far, click on the stop icon to stop execution. Click on the reset icon to reset the location pointer back to 0.

Click on the debug icon to leave the debugger and get back to the editor window. Click on the open file icon (or select File/Open from the menu) and open the LST file for your source code. You will need to change the 'files of type' list box to display LST files first. Notice that the listing file is date stamped and displays the A51 command line used to invoke the assembler. In this case you should see the name of your source file, the 'small memory model' option, and some other options. To see what these options mean, click on the 'books' tab in the project window, and double click on the A51 User's Guide which will open the user's guide PDF in Adobe Acroread. Go to Appendix C in that manual for a list of controls.

The source code with line numbers and machine code added is displayed next, followed by the symbol table. This is a list of the symbols in your program along with their values. For example, ACC is a data type address with value E0 absolute. The complete format of the listing file is documented in Appendix F of the user's guide. Chapter 1 of this manual gives a nice overview of assembly language programming.

### Summary

This example has shown:

- How to use µvsion2 to create an assembly language application
- How to use the µvision2 debugger to test an assembly language application

This has been a brief introduction to assembly language application development with Keil's µvsion2. Those who wish to dig deeper are encouraged to read the A51 user's guide.

stop icon