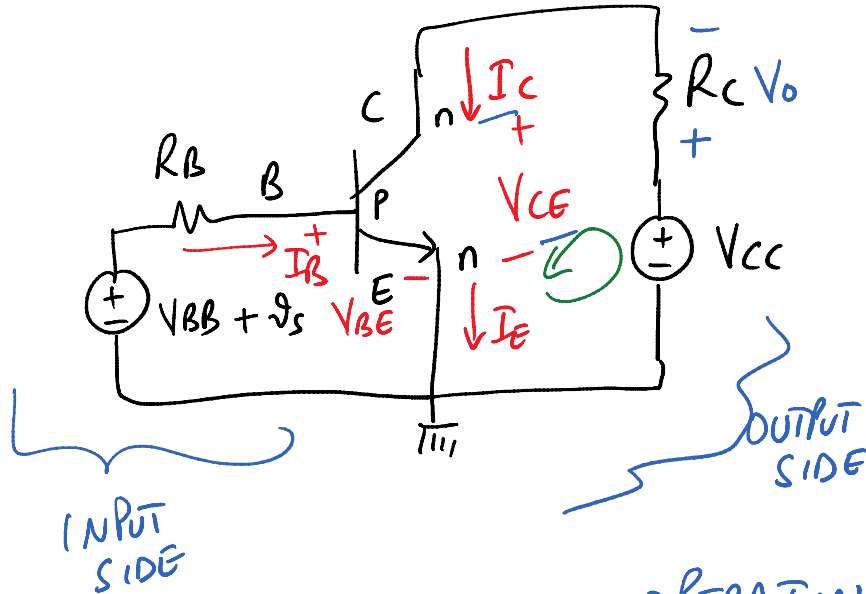


LECTURE-2B

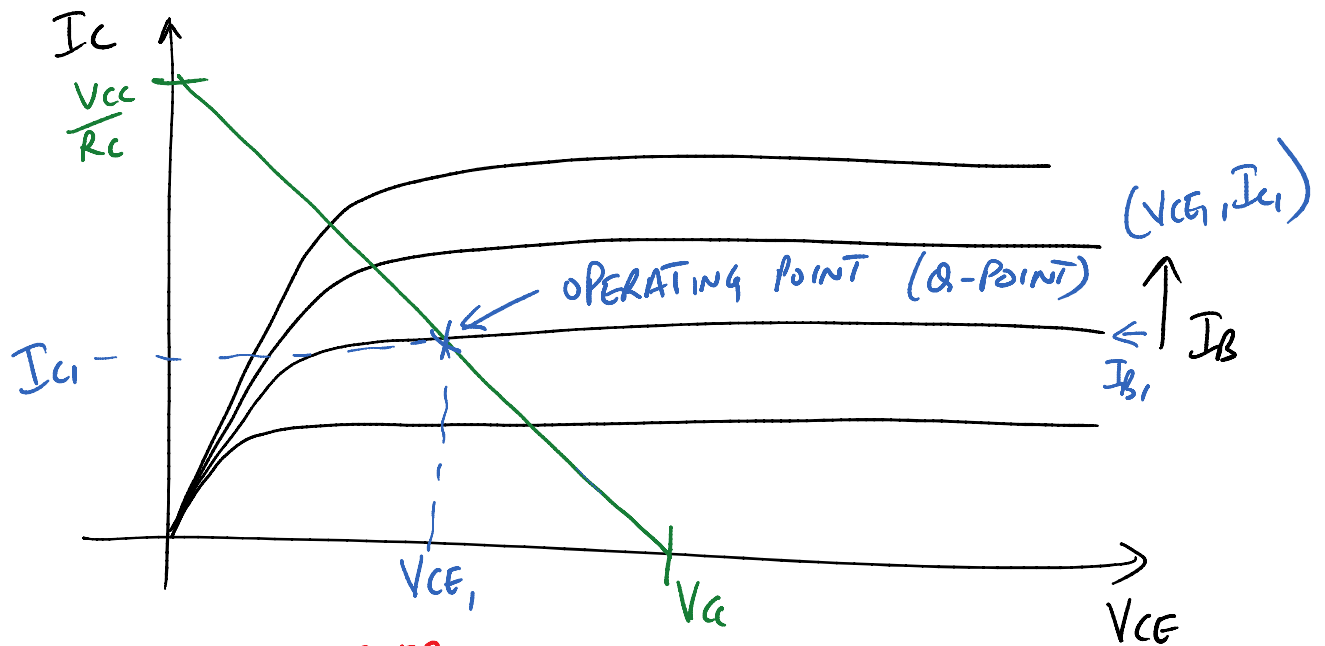
COMMON EMITTER BJT CIRCUIT



ACTIVE REGION OPERATION

BE	JUNCTION IS FB	$V_{BE} \geq V_{\gamma}$ (TURN-ON VOLTAGE)
CB	" IS RB	$V_{BC} < 0$ OR $V_{CB} > 0$

OPERATING POINT (V_{CE}, I_C)



LOAD LINE EQ.

KVL CE LOOP

$$-V_{CC} + I_C R_C + V_{CE} = 0$$

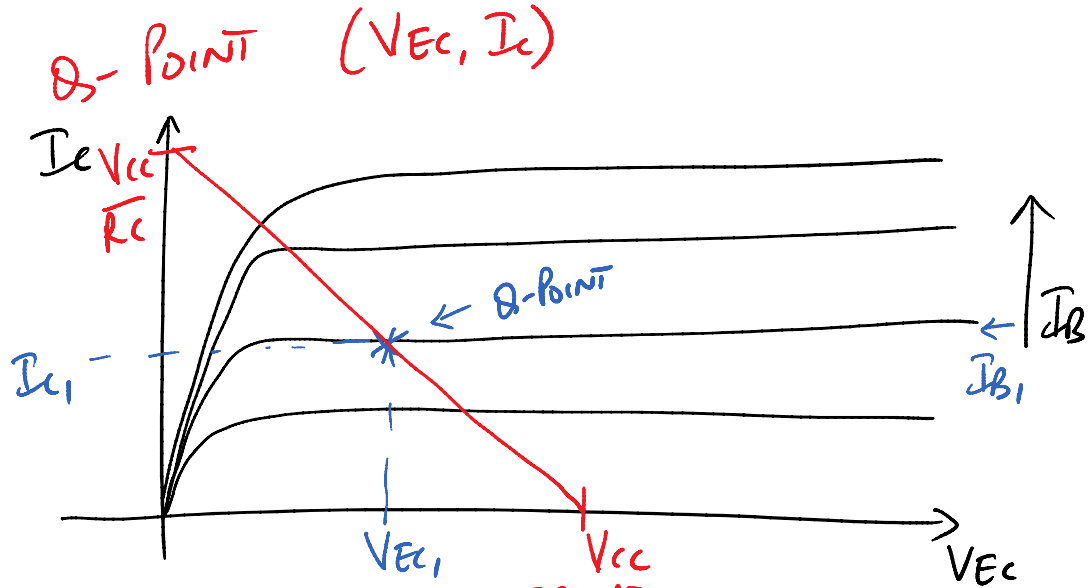
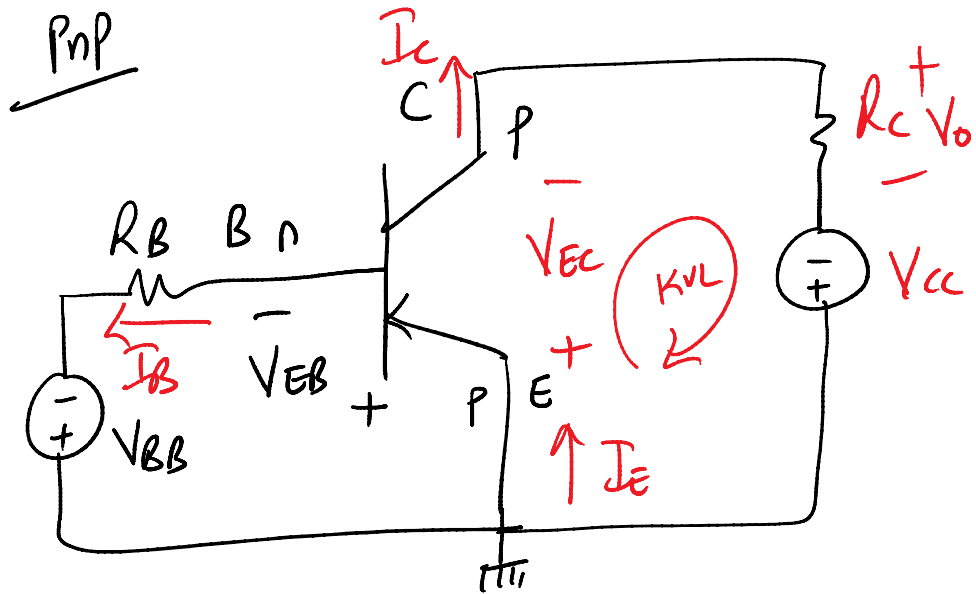
$$V_{CE} = V_{CC} - I_C R_C$$

$$V_{CE} = 0$$

$$I_C = \frac{V_{CC}}{R_C}$$

$$I_C = 0$$

$$V_{CE} = V_{CC}$$



LOAD LINE EQUATION

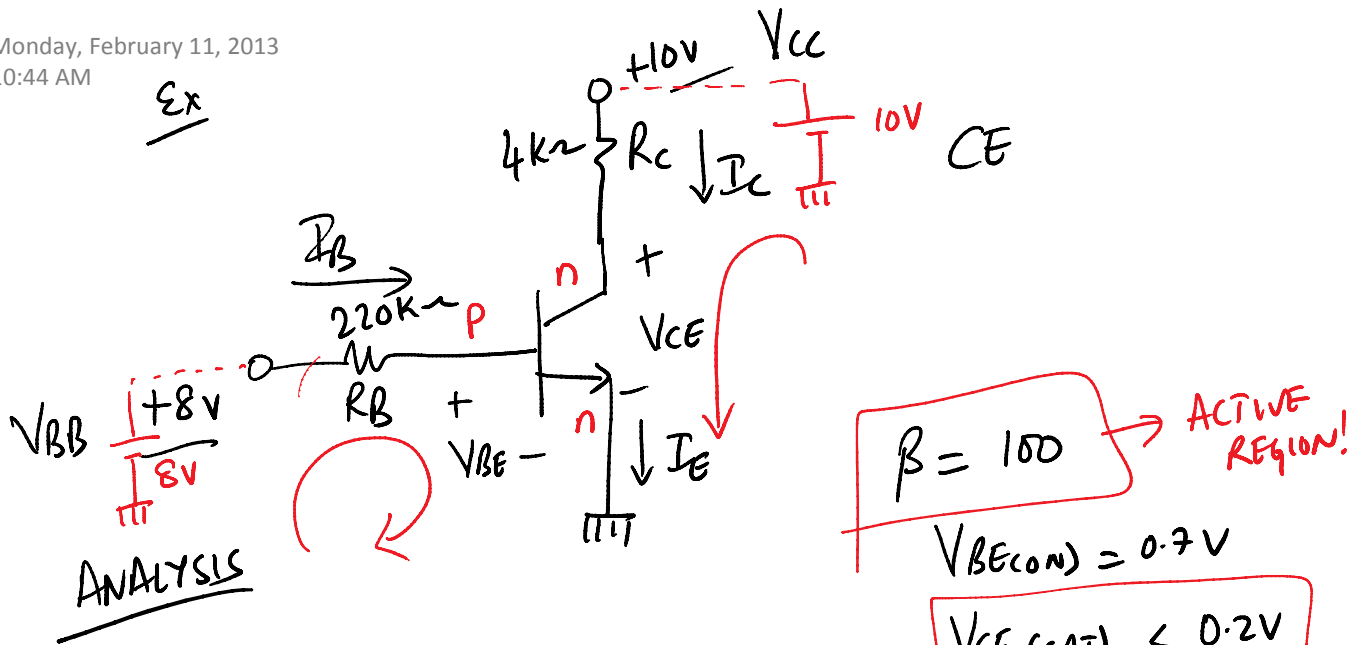
KVL EC LOOP (output)

$$+V_{EC} + I_C R_C - V_{CC} = 0$$

$$V_{EC} = V_{CC} - I_C R_C$$

$$V_{EC} = 0 \quad I_C = \frac{V_{CC}}{R_C}$$

$$I_C = 0 \quad V_{EC} = V_{CC}$$



* DO NOT KNOW THE REGION OF OPERATION
ASSUME BIAS IS IN THE ACTIVE REGION

+8V ON BASE SIDE ∴ TRANSISTOR IS ON!
∴ BE JUNCTION IS FB $V_{BB} > V_{to} = 0.7V$
 $V_{BE(on)}$

KVL BE LOOP

$$-8 + I_B R_B + V_{BE} = 0$$

$$I_B = \frac{8 - 0.7}{220k} = \underline{\underline{33.2 \mu A}}$$

$$I_C = \beta I_B$$

$$= 3.32 \text{ mA}$$

ONLY VALID IF BJT IS IN ACTIVE REGION

KVL CE LOOP

$$-10 + I_c R_c + V_{CE} = 0$$

$$V_{CE} = 10 - (3.32\text{mA})(4\text{k})$$

$$= -3.28\text{V}$$

X NOT POSSIBLE!

∴ TRANSISTOR IS IN SATURATION!

RE DO THE ANALYSIS AND START FROM O/P SIDE

$$V_{CE} = V_{CE(\text{SAT})} = \underline{\underline{0.2\text{V}}}$$

KVL CE LOOP

$$I_c = I_{c(\text{SAT})} = \frac{V_{CC} - V_{CE(\text{SAT})}}{R_c} = \frac{10 - 0.2}{4\text{k}} = 2.45\text{mA}$$

$$V_{BE} = 0.7\text{V}$$

$$\therefore I_B = 33.2\text{ }\mu\text{A}$$

$$\frac{I_c}{I_B} = \frac{2.45\text{mA}}{33.2\text{ }\mu\text{A}} = \boxed{74} < \beta$$

β_{FORCED} IN SATURATION

DEEP SATURATION \rightarrow $V_{BE} \uparrow \uparrow$ $I_B \uparrow \uparrow$ I_c SAME

$$\therefore \frac{I_c}{I_B} \downarrow \downarrow \therefore \beta_{\text{FORCED}} \downarrow \downarrow$$

Monday, February 11, 2013
10:55 AM

$$I_E = I_B + I_C = 2.45\text{m} + 33.2\mu = 248\text{mA}$$

POWER DISSIPATED IN TRANSISTOR

$$P_T = I_B V_{BE(\text{ON})} + I_C V_{CE}$$

$$= (33.2\mu) (0.7) + (2.45\text{m}) (0.2)$$

$$= \underline{\underline{0.513\text{mW}}}$$

STEPS TO ANALYZE

① ASSUME TRANSISTOR IS BIASED IN FORWARD ACTIVE REGION $\therefore V_{BE} = V_{BE(ON)}$, $I_B > 0$,
 $I_C = \beta I_B$

② ANALYZE THE CIRCUIT

③ IF $V_{CE} > V_{CE(SAT)}$, THEN O.K

④ IF $I_B < 0 \rightarrow$ CUT-OFF $I_B = 0$, $I_C = 0$, $V_{CE} = V_{CC}$

IF $V_{CE} \leq V_{CE(SAT)} \rightarrow$ SATURATION $V_{CE} = V_{CE(SAT)}$

⑤ MAKE NEW ASSUMPTION AND SOLVE I_C, I_B , AND β FORCED